



University of Hagen Department of Mathematics and Computer Science Chair of Enterprise-wide Software Systems



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# **Motivation**

- Manufacturing of integrated circuits is one of the key aspects of the electronics industry
- Complex scheduling problems attracted researchers and people from industry for the last three decades
- ▷ Causes: high degree of automation even 30 years ago
  - automated real-time data collection
  - manual production control leads often to an unexpected behavior of the system



- Increasing automation pressure by automated material handling systems (AMHS) and new requirements on production control (Industry 4.0)
- Deterministic machine scheduling approaches are crucial for wafer fabs

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# Outline

- Motivation
- Process Description
- Scheduling Problems
- Factory Scheduling

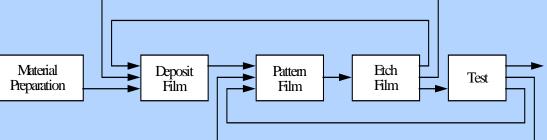
Part 1

- Recap: Semiconductor Process Description
- Batch Scheduling Problems
- Scheduling Problems with Time Constraints
- Multiple Orders per Job Scheduling Problems
- Future Research Directions

# **Recap: Semiconductor Process Description**

- ▷ many process flows
- ▷ each process flow contains up to 800 operations
- ▷ several hundreds of machines (often very expensive)
- ▷ unrelated parallel machines with dedications
- ▷ re-entrant flows





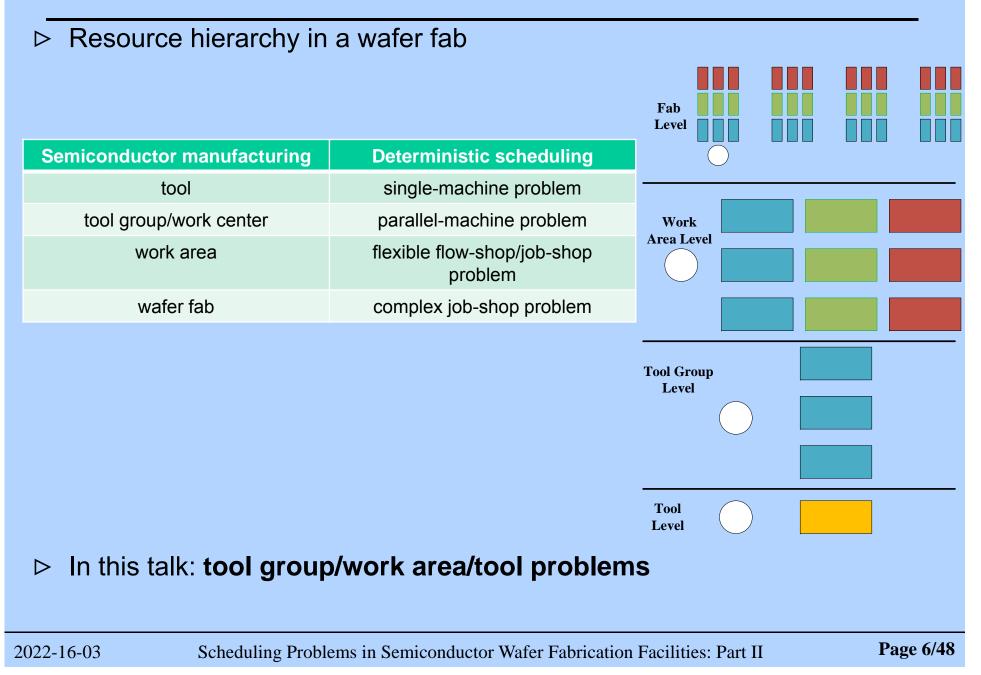
- ▷ time constraints for the processing of jobs to prevent native oxidation and contamination effects on the wafer surface
- some machines like implanters require significant sequence-dependent setup times
- ▷ dynamic bottlenecks depending on the product mix

#### **Recap: Semiconductor Process Description (cont'd)**

- some process steps require auxiliary resources such as reticles in photolithography
- ▷ different processing times
- Iong operations often involve batch processes (one third of all operations might be batch operations)
- In non-linear flow because of mixture of batching and non-batching machines => long queues in front of the machines
- probabilistic occurrence of long machine failures leads to a large variability



# **Recap: Semiconductor Process Description (cont'd)**



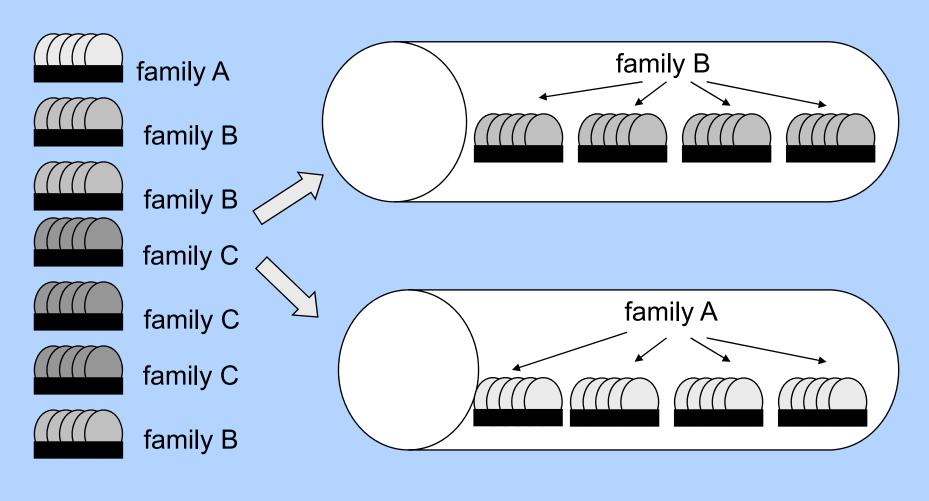
# **Batch Scheduling Problems**

- Batch = group of jobs that have to be processed together
- Completion time of a batch = completion time of the last job of the batch
- ▷ Two types of batching problems (with batch availability):
  - s-batching: processing time of the batch is the sum of the processing times of the jobs
  - p-batching: processing time of the batch is the maximum processing time of the jobs that form the batch
  - compatible vs. incompatible setting
- ▷ p-batching is very important in semiconductor manufacturing
- maximum batch size B

#### ▷ Examples:

- burn-in ovens are used to heat-stress test chips
- diffusion furnaces => incompatible job families, only jobs of one job family can be batched together due to the chemical nature of the process
- ▷ Fowler, J., Mönch, L. (2022): A survey of scheduling with parallel batch (p-batch) processing. EJOR, 298(1), 1-24.

Example: three incompatible families, B=4



- ▷ joint work with Sebastian Roob
- ▷ Scheduling problem:

$$P_m / p - batch, incompatible / \sum_{j=1}^n f_j(C_j)$$
 (1)

- $\triangleright$  *f* regular sum objective function
- Incompatible families: all jobs of the same family have a common processing time

#### ▷ **Property:**

There exists an optimal schedule for each instance of problem (1) that does not contain partially full batches except possibly the last batch of each family to be processed in the schedule.

▷ The number of batches for each family can be precomputed.

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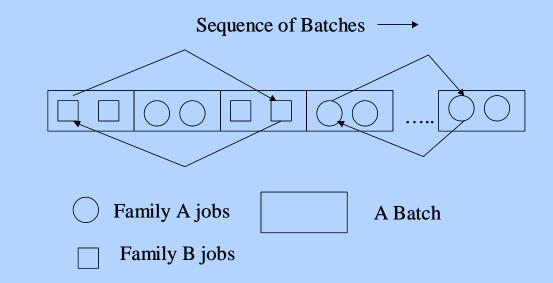
- 1. How to form batches?
- 2. How to assign the batches to one of the parallel machines?
- 3. How to sequence the batches on each of the parallel machines?
- Swap is used in Balasubramanian et al. (2004), Almeder and Mönch (2011), Lausch and Mönch (2016) for correcting the content of batches

#### ▷ Swap-Iter:

- 1. Start with the first job of the batch with the smallest start time.
- 2. Try to interchange the current job with each job that is found in a batch of the same family that starts later.
- If an objective function value reduction occurs because of the job swap, exchange the two jobs and start over with the first job of the batch.

▷ Works for single and parallel machines, only starting times of

the batches of a given family are of interest



Observation: If starting time of batches are known, the cost (objective function value) for assigning a job to any batch can be calculated.

Swap can be modeled as the following assignment-type problem for each incompatible family *f*:

$$\min \sum_{b=1}^{b_f} \sum_{j=1}^{n_f} f_j(C_b) x_{jb}$$
  
subject to  
$$\sum_{\substack{b=1 \\ n_f}}^{b_f} x_{jb} = 1, \quad j = 1, \dots, n_f$$
  
$$\sum_{\substack{j=1 \\ x_{jb} \in \{0, 1\}, \quad j = 1, \dots, n_f, b = 1, \dots, b_f}$$

Observation 1: Since the coefficient matrix is totally unimodular, the IP is easy to solve by the Simplex algorithm.

Observation 2: IP can be reformulated as a minimum cost flow (MCF) problem =>

more efficient solution methods compared to the conventional Simplex algorithm are available in solver software, i.e., the network simplex algorithm or the out-of-kilter algorithm

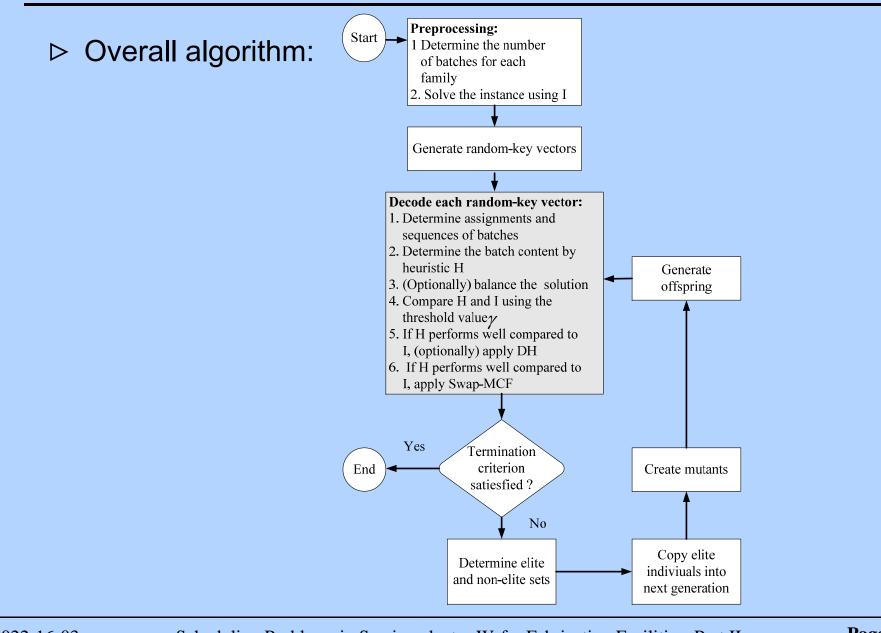
#### => Swap-MCF

- ▷ It is enough to assign and sequence "empty" batches
- ▷ Computing the optimal content of the batches is "easy" task
- Matheuristic: biased random-key genetic algorithm (BRKGA) to assign and sequence batches
- ▷ Representation: RK =  $[rk_1, rk_2, ..., r_b]$
- ▷ Random keys
  - integer part: determines machine
  - fractional part: determines sequence
- ▷ Uniform crossover
- ▷ Immigration strategy for mutation

- ▷ Applying Swap-MCF for each chromosome time-consuming
- ▷ Reference heuristic *I* based on list scheduling
- Heuristic *H* to compute the content of batches for each chromosome, followed by a workload balancing procedure
- $\triangleright$  Consider threshold value  $\gamma > 0$ :

$$\sum_{j=1}^{n} f_{j}\left(C_{j}\left(H\right)\right) \leq \gamma \sum_{j=1}^{n} f_{j}\left(C_{j}\left(I\right)\right) \quad (2)$$

- $\triangleright$  If (2) holds => improve the schedule by
  - (optionally) a decomposition heuristic (DH) for each single machine
  - Swap-MCF



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Scheduling Problems in Semiconductor Wafer Fabrication Facilities: Part II

▷ Application of the framework to:

$$1/p-batch, incompatible / \sum_{j=1}^{n} w_{j}U_{j}$$
 (3)

- ▷ Dauzere-Peres & Mönch (2013):
  - specific MILP formulations
  - RKGA based on job sequences
- ▷ Tailoring of the framework:
  - I: list scheduling based on weighted modified due date (WMDD) rule
  - H: modification of this approach for given batch set

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▷ Application of the framework to:

$$P_m / p - batch, incompatible / \sum_{j=1}^n w_j T_j$$
 (4)

- Almeder & Mönch (2011)/Lausch & Mönch (2016):
  - ACO
  - VNS
  - LNS
- ▷ Tailoring of the framework:
  - I: list scheduling based on apparent tardiness cost (ATC) rule
  - H: modification of this approach for given batch set

#### Implementation issues:

- brkga-API framework, C++ programming language
- Ip\_solve 5.5

- 144 small-sized and 144 large-sized instances from Dauzere-Peres & Mönch (2013)
- ▷ Up to 144 jobs, up to 6 families, up to 90% tardy jobs
- ▷ Computational results (60 sec computing time per instance):
  - 72 small-sized unweighted instances: optimal solutions known from Dauzere-Peres & Mönch (2013) are found
  - 72 small-sized weighted instances: only 0.97% worst compared to the best performing MILP approach from Dauzere-Peres & Mönch (2013)
  - 72 large-sized unweighted instances: 0.31%
  - 72 large-sized weighted instances: 1.82%

- ▷ 288 problem instances from Almeder & Mönch (2011)
- ▷ Up to 300 jobs, up to 12 families, up to 60% tardy jobs
- ▷ Average deviation from results obtained by VNS/LNS is 1.33%
- ▷ different schedules:
  - **low-quality** solutions
  - medium-quality solutions
  - fairly high-quality solutions
- ▷ Solution quality of Swap-Iter: percentage of optimal solutions

Low-quality Schedules	Medium-quality Schedules	<b>High-quality Schedules</b>
16.94%	36.39%	36.18%

- ▷ TWT ratios are 0.98 for low-quality and 0.99 for high-quality schedules
- ▷ Swap-MCF requires only 70% computing time of Swap-Iter

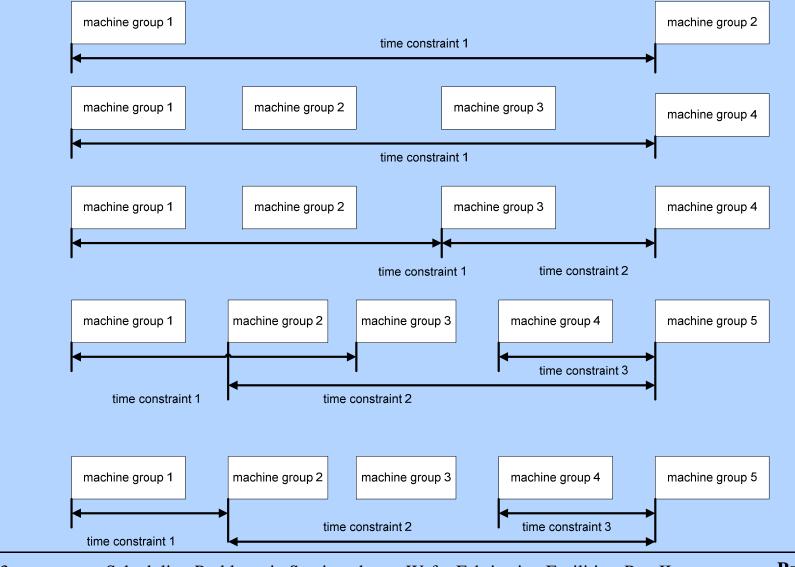
▷ **Definition:** We consider job  $J_i$  from product *j*. A time constraint holds for the operations  $O_{ir}$  and  $O_{is}$  if we have for the corresponding start times for r < s:

$$s_{is} \leq s_{ir} + t_{irs}.$$

#### ▷ scheduling literature:

- only the very special case: s = r+1 maximal time lag
- minimal time lag: modeling of transportation times
- Caumond *et al.* (2008), Grimes and Hebrard (2010), Jung *et al.* (2014), Klemmt and Mönch (2012)
- different classes of time constraints can be found in semiconductor manufacturing
- nested time constraints

#### ▷ classes of time constraints found in wafer fabs



Scheduling Problems in Semiconductor Wafer Fabrication Facilities: Part II

 $\triangleright$  simplified model problem:  $FF_m | r_i, t_{ip}, t_{jop} | TWT$ 

where  $T := \{(j, o, p) | 1 \le o is the set of all time constraints and <math>t_{ip}$  is an initial time constraint.  $T^{(i)} := \{(i, p) | 1 \le p \le m\}$  is the corresponding set.

- ▷ notation:
  - $d_i$ : due date of job  $J_i$
  - $r_i$  : release date of job  $J_i$
  - $C_i$ : completion time of job  $J_i$
  - $T_i$ : tardiness of job  $J_i$
  - r<sub>ol</sub>: availability of machine M<sub>ol</sub>

▷ MILP formulation: (main) decision variables:

- $s_{io} \in IR_+$  : starting time of operation *o* of job  $J_i$
- $x_{igo} \in \{0,1\}$ : 1 if operation *o* of  $J_i$  is scheduled before operation *o* of  $J_g$
- $w_{iol} \in \{0,1\}$  : 1 if operation *o* of  $J_i$  is scheduled on machine *I*

$$\min \sum_{i=1}^{n} w_i T_i$$
  
subject to

$$\begin{aligned} C_{i} - T_{i} &\leq d_{i}, & i = 1, \dots, n \\ s_{io} + p_{jo} &\leq C_{i}, & i = 1, \dots, n, o = 1, \dots, m, j = f(i) \\ r_{ol} w_{iol} &\leq s_{io}, & i = 1, \dots, n, o = 1, \dots, m, l = 1, \dots, m_{o} \\ r_{i} &\leq s_{i1}, & i = 1, \dots, n \end{aligned}$$

$$\begin{split} &\sum_{l=1}^{m_o} w_{iol} = 1, \qquad i = 1, \dots, n, o = 1, \dots, m \\ &S_{io} + p_{jo} \leq S_{i,o+1}, \qquad i = 1, \dots, n, o = 1, \dots, m-1, \ j = f(i) \\ &K(w_{iol} - x_{igo} - 1) + s_{go} + p_{jo} w_{gol} \leq s_{io}, \qquad o = 1, \dots, m, l = 1, \dots, m_o, 1 \leq i < g \leq n, \ j = f(g) \\ &K(w_{gol} + x_{igo} - 2) + s_{io} + p_{jo} w_{iol} \leq s_{go}, \qquad o = 1, \dots, m, l = 1, \dots, m_o, 1 \leq i < g \leq n, \ j = f(i) \\ &S_{iq} \leq s_{io} + t_{joq}, \quad (j, o, q) \in T, \ i = 1, \dots, n, \ j = f(i) \\ &S_{ip} \leq t_{ip}, \qquad (i, p) \in T^{(i)}, \ i = 1, \dots, n. \end{split}$$

- Up to 10 jobs can be solved on a small number of machines to optimality in a reasonable amount of time.
  - => decomposition approaches

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# I. List scheduling approach with backtracking

- ▷ job-by-job decomposition approach to determine "canonical" schedules
- **1. Initialization:** Discretize the time axis. Sort all jobs with respect to a (global) ATC dispatching rule.
- **2.** Schedule the first job in ATC order taking into account the time constraints.
- **3. Repeat** for the remaining jobs:
  - a) Repeat for all stages.
  - b) Determine the machine with the smallest availability time for the current stage. If the current operation can be started without time constraint violation then schedule it and go to a).
  - c) Otherwise, do a right shift of the starting time of the earlier operation that contributes to the time constraint by increasing it by one unit and try to schedule the operations taking into account other time constraints until the current one that leads to the infeasibility.

- II. Decomposition approach based on the MILP/CP formulation
- **1.** Initialization: Sort the jobs with respect to the ATC rule. Denote the set of scheduled jobs by S. Set  $S := \emptyset$ . *L* is the set of unscheduled jobs.
- **2.** Repeat for *I* that contains the first  $\min(|L|, \overline{n})$  unscheduled jobs in ATC order
  - a) Schedule the job set *I* using the MILP/CP with an objective function that contains the sum of the start times as a penalty term.
  - b) Update the machine availability and the sets S and L and go to Step 2.

#### $\triangleright \quad \overline{n} \leq 6$

- Problem instances with up to 100 machines that are organized in up to 20 stages can be solved.
- ▷ All classes of nested time constraints can be treated by this approach.

#### **III.** Decomposition approach based on genetic algorithm

- 1. Replace ATC ordering by sequencing based on BRKGA, random-key representation
- 2. Use list scheduling approach with backtracking for each chromosome to determine fitness
- 3. Use MILP/CP decomposition approach to improve best sequence obtained from BRKGA
- ▷ Implementation based on brkgaAPI (Toso & Resende 2014)

- ▷ design of experiments
- ▷ randomly generated problem instances
- ▷ performance depends on
  - number of jobs
  - number of stages
  - number of parallel machines on each stage
  - ready time and due date setting
  - time constraint class and tightness of the time constraints
- ▷ two scenarios:
  - class 3
  - class 4, but only a single overlap of time constraints is allowed
- totally, 40 problem instances with up to 100 jobs and 72 instances with up to 750 jobs
- $\triangleright \overline{n} \coloneqq 5$
- maximum computing time per MILP/CP instance 5 seconds (ILOG CPLEX 12.7)

▷ Improvement in % over list scheduling with backtracking

Factor level	ATC+ CP(5)	BRKGA(300s) +MILP(5)	BRKGA(300s) +CP(5)
n=50	11.78	21.68	21.54
n=100	12.58	24.71	24.79
n=125	2.03	8.84	8.88
n=150	5.85	19.72	19.94
n=250	7.53	15.95	18.00
n=375	5.41	11.44	12.93
n=500	2.48	10.32	11.14
n=750	1.96	7.28	7.94
Overall	8.43	17.99	18.50

- ▷ BRKGA+CP slightly outperforms BRKGA+MILP approach
- ▷ large number of jobs => improvements are smaller
- Ist scheduling with backtracking is outperformed by BRKGA-type approaches, post-optimization by CP beneficial
- Improvement for the TWT setting is much smaller compared to improvement for TT setting

extensions for flexible flow-shops with batch processing machines by Cailloux and Mönch (2019)

# **Multiple Orders per Job Scheduling Problems**

# Joint work with Jens Rocholl

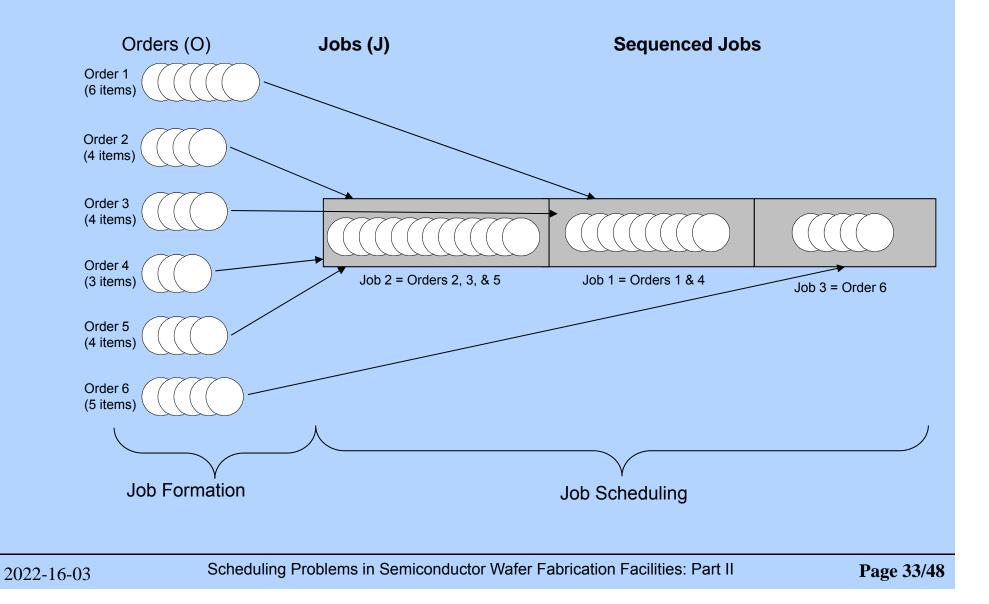
- Front-opening unified pod (FOUP) = standard unit of lot transfer between machines, container that holds up to 25wafer lots of 300-mm wafers in an inert, nitrogen atmosphere
- ▷ FOUPs are expensive



- ▷ A large number of FOUPs can cause a congested AMHS
- Combination of decreased line width and increased area per wafer => fewer wafers are required to fill the ICs of a certain customer
- Need to group orders of different customers into one FOUP => Multiple Orders per Job (MOJ) scheduling problem

## **Multiple Orders per Job Scheduling Problems**

#### ▷ MOJ problems



# Multiple Orders per Job Scheduling Problems (cont'd)

- ⊳ FOUPs: *F* 
  - capacity K
- $\triangleright$  Orders o=1,...,N
  - $s_0$ : size (number of wafers),  $s_0 \le K$
  - do: due date
- ⊳ Jobs: *j*=1,...,|*J*|
  - $|J| \leq \min(F, N)$
  - capacity  $K, \sum s_0 \le K$
  - processing time p<sub>j</sub>

# Multiple Orders per Job Scheduling Problems (cont'd)

- ▷ Two environments:
  - Lot processing: all wafers of a job will be processed simultaneously, p<sub>j</sub> = ρ processing time of job j
  - Single item processing: product of wafer processing time and number of wafers, p<sub>j</sub> =ρ∑s₀

**Problem (1):**  $E/T = \sum |C_o - d| = \sum E_o + \sum T_o$ 

 $1|moj(lot), d_{o}=d|E/T, d$  common due date,

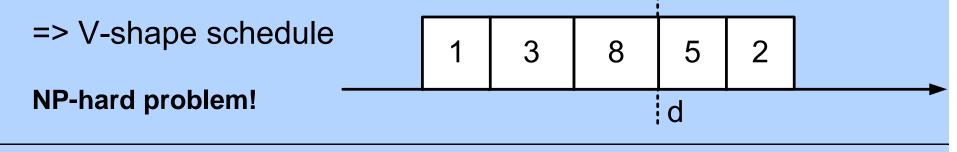
 $\sum p_0 \leq d$ , nonrestrictive

Multiple Orders per Job Scheduling Problems (cont'd)

- ▷ Properties of problem (1)
- **1. Property:** There is an optimal schedule where one job is completed at the common due date *d*.

A: set of early or on-time jobs (= early or on-time orders)B: set of tardy jobs (= tardy orders)wj: number of orders in job *j* 

**2. Property:** There is an optimal schedule where all jobs in A are sorted with respect to increasing w<sub>j</sub>/p<sub>j</sub> and all jobs in B with respect to decreasing w<sub>j</sub>/p<sub>j</sub>.



- $\triangleright$  IP formulation for problem (1)
- ▷ Basic idea (Hall and Possner 1991):

 $C_j = d - [|J|/2] \rho + j \rho =>$ 

 $E/T_j = \rho \max([|J|/2] - j, 0) + \rho \max(-[|J|/2] + j, 0)$ 

$$\min \sum_{o=1}^{N} E/T_{j}X_{oj}$$
  
subject to  
$$\sum_{\substack{j \in J \\ N}} X_{oj} = 1, \qquad \forall o \in O$$
  
$$\sum_{o=1}^{N} s_{o}X_{oj} \leq K, \qquad \forall j \in J$$
  
$$X_{oj} \in \{0,1\}, \forall o \in O, \forall j \in J$$

## ▷ MOJ scheduling problems:

- Mason *et al.* a series of papers starting in 2002
- Qu & Mason (2005) for problem (1) => two GAs
- Sobeyko & Mönch (2015) => GGA

#### > nonrestrictive common due date and p-batching:

- Kanet (1984), polynomial-time algorithm for 1|d<sub>i</sub>=d|E/T
- Hall & Posner (1991), total weighted earliness and tardiness, DP
- Mönch & Unbehaun (2007) Pm|p-batch,dj=d|E/T => GAs hybridized with DP
- Li *et al.* (2015) 1|p-batch,s<sub>j</sub>,d<sub>j</sub>=d|E/T => GA hybridized with DP

- ▷ **Simple reference heuristic** similar to Mason *et al.* (2004):
- 1. Sort the orders according to a priority rule (i.e. Smallest Size (SS), i.e. 1/s<sub>0</sub> or Largest Size (LS), i.e. s<sub>0</sub>)
- 2. Assign orders to jobs according to First Fit Decreasing FFDn, i.e., assign an order to the first job with available capacity.
- Sort jobs in non-increasing order of the number of orders in the jobs and determine V-shaped schedule by assigning jobs to the sets A and B

(**|J|-1,..,3,1**#2,4,...,|J|) or (**|J|,..,3,1**#2,4,...,|J|-1)

### ▷ First GA (GA-H)

- Idea: GA assigns orders to A and B
- Representation:  $c:=(a_1,...,a_N)$  and  $a_j := \begin{cases} 1, \text{if } o_j \in A \\ 0, \text{if } o_j \in B \end{cases}$
- Compute solution by applying SS-FFDn or LS-FFDn to A, B
- One-Point Crossover + Flip Mutation
- => GA-H1 and GA-H2

#### Second GA (RKGA)

- Idea: GA produces permutations of the orders
- Random-key representation: c:=(a₁,...,aℕ) with a<sub>j</sub>∈[0,1]
- order sequence by  $O_i \leq O_k$  if  $a_i \leq a_k$
- Parameterized uniform crossover, i.e. biased coin for each gene to determine which gene contributes to the child
- No mutation, but immigration of new randomly generated individuals
- Permutation of orders => FFDn + V-shaped schedule

Factor	Level		
S <sub>o</sub>	$\sim DU\left[\nu - \frac{\nu+1}{2}, \nu + \frac{\nu+1}{2}\right], \nu \in \{3,5\}$	2	
Wo	~ <i>DU</i> [1,15]	1	
K (in wafer)	$12\beta + 1, \ \beta \in \{1,2\}$	2	
N	10,25,50,100	4	
F	$\left\lceil N\nu / 12\beta \right\rceil + 1$	1	
Processing time	ρ=10	1	
Number of independent problem instances	10 per factor combination	10	
Total number of problem instances		160	

## ▷ 160 instances

TABLE I COMPUTATIONAL RESULTS						
N	β	ν	GA-H1	GA-H2	RKGA	
10	1	3	35	38	35	
	1	5	n/a	91	91	
	2	3	6	6	6	
	2	5	41	41	41	
25	1	3	221	227	219	
	1	5	n/a	515	505	
	2	3	118	121	118	
	2	5	248	248	246	
50	1	3	908	951	902	
	1	5	n/a	2023	1987	
	2	3	479	490	479	
	2	5	n/a	1019	1020	
100	1	3	3700	3924	3718	
	1	5	n/a	8173	8057	
	2	3	1865	1935	1935	
	2	5	n/a	n/a	4080	

- SS-FFDn => often not able to provide feasible solution
- ▷ GA-H1 similar problems
- RKGA works well for small- and medium-sized problem instances
- ▷ GA-H2 is best performer for large-sized instances
- CPLEX is able to solve instances up to 25 orders to optimality
- RKGA was always able to find these optimal solutions

#### **Future Research Directions**

#### ▷ New problem settings:

- 1. borderline between dispatching and scheduling must be explored in more detail
- 2. assumption of deterministic data is not always true, stochastic settings must be considered
- 3. scheduling problems with interfering job sets since production lots compete with engineering lots for scarce resources
- 4. better interaction of AMHS decisions and job scheduling decisions, storing and transporting lots must be done consistently with their production schedule
- interaction of scheduling decisions and Advanced Process Control (APC), new constraints for scheduling to improve quality and yield
- 6. better understanding of the interplay between production planning and scheduling decisions for wafer fabs is desirable

#### ▷ New solution techniques:

- more theoretical insights are necessary related to hierarchical and non-hierarchical decomposition schemes, i.e., consistency between global wafer fab scheduling and detailed work area scheduling
- 2. hybrid approaches based on MILP, CP, and metaheuristic approaches, especially for fab-wide scheduling
- 3. common benchmarks are desirable like for traditional job shop scheduling problems
- 4. ideas from stochastic programming and robust optimization can be used to tackle scheduling problems in a stochastic setting
- 5. promising trend is the use of genetic programming to discover dispatching rules
- more research is needed to investigate whether the solution process can be accelerated using the Compute Unified Device architecture (CUDA) platform or Graphics Processing Unit (GPU) programming approaches

# **Future Research Directions (cont'd)**

- ▷ Survey paper:
  - Mönch, L., Fowler, J. W., Dauzère-Pérès, S., Mason, S. J., Rose, O. 2011. A survey of problems, solution techniques, and future challenges in scheduling semiconductor manufacturing operations. *Journal of Scheduling*, 14(6), 583-599.
  - Updated version is planned for 2022.



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Scheduling Problems in Semiconductor Wafer Fabrication Facilities: Part II

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